	Application No.	Applicant(s)
Notice of Allowability	09/675,067	HUANG, SAMSON X.
	Examiner	Art Unit
	Fritz Alphonse	2133
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>notice of appeal filed 8/22/05.</u>		
2. The allowed claim(s) is/are <u>1-43</u> .		
3.		
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Date 98), 7. Examiner's Amendre	e
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## **DETAILED ACTION**

0.1 This office action is in response to Notice of Appeal filed on 8/22/2005. Claims 1-43 are pending.

## **REASONS FOR ALLOWANCE**

- 1. After further search and thorough examination of the present application claims 1-43 are found to be in condition for allowance over the prior art of record.
- 2. The following is an Examiner's statement of reasons for allowance: the claimed invention provides a mechanism to repair memories by utilizing the least significant bit of storage to store more significant information when the memory cell for the more significant information is faulty.

To achieve the inventive goal, independent claims 1, 9, 15, 20, 26, 32 and 38, identify the distinct features "a repair router to utilize the least significant data bit of at least one of the plurality of addresses to hold non-least significant information from any of the plurality of non-least significant data bits and to discard least significant information"

The first cited reference, Nishikawa (U.S. Pat. No. 5,805,604) discloses a memory circuit having a plurality of regions for reading and writing data. The second cited reference, Shichiku et al. (U.S. Pat. No. 6,317,817) disclose an image operation processing apparatus that processes data packet including instruction information and an address of a prescribed address space.

However, none of the cited references, either singular or in combination teach or fairly suggest to discard the least significant bit of the repair router input data bus.

These limitations, in conjunction with all other limitations of the base claims were not shown by, would not have been obvious over, would have been fairly suggested by the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

3. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

or faxed to: (703) 872-9306 for all formal communications.

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Fourth Floor (Receptionist).

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fritz Alphonse, whose telephone number is (571) 272-3813. The examiner can normally be reached on M-F, 8:30-6:00, Alt. Mondays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert De Cady, can be reached at (571) 272-3819.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-3824.

Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications

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may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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September 28, 2005

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